

Case Docket No. ADTST.031AUS

Date: August 28, 2001

Page 1

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

ATTENTION: APPLICATION BRANCH

Sir

Transmitted herewith for filing is the patent application of

Inventor(s): Hiroaki Yamoto et al.

For: METHOD FOR DESIGN VALIDATION OF COMPLEX IC

Enclosed are:

- (X) 7 sheet(s) of drawings.
- (X) 23 pages of specification, claims and abstract.
- (X) UNSIGNED declaration and power of attorney.
- (X) Return prepaid postcard.

CLAIMS AS FILED				
FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
Basic Fee			\$710	\$710
Total Claims	20 - 20 =	0 ×	\$18	\$0
Independent Claims	4 - 3 =	1 ×	\$80	\$80
If application contains any multiple dependent claims(s), then add \$270				\$0
	TOTAL FILING FEE			\$790

(X) A check in the amount of \$790 to cover the filing fee is enclosed.

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August 28, 2001

Assistant Commissioner for Patents Washington, D.C. 20231

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Attorney Docket No.

ADTST.031AUS

Applicant(s)

Hiroaki Yamoto et al.

For

METHOD FOR DESIGN VALIDATION OF

COMPLEX IC

Attorney

Yasuo Muramatsu

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August 28, 2001

I hereby certify that the accompanying

Transmittal; a patent application in 23 pages of specification and 7 sheets of drawings; a check of \$790 for filing fee; Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Jano Muramatsu

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